

6) **NEURAL NETWORKS: ANALOG VLSI ARCHITECTURES AND IMPLEMENTATIONS**

G.M. Bo, D. Baratta, F. Camboni, D.D. Caviglia, F. Diotalevi, M. Valle

Collaborations: ATMEL, ELSAG-Bailey, Università di Parigi Sud, ICE-CNR Genova, Universita Publica de Navarra, Département d'électricité - Université Catholique de Louvain (Belgium), IRST Trento, COREP Torino, IMEC (Belgio), Ansaldo Ricerche, Alcatel Microelectronics, ASL

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Area: Integrated Circuits and Systems

7) **HIGH-LEVEL SYNTHESIS OF VLSI SYSTEMS**

F. Curatelli, L. Mangeruca, M. Chirico

Collaborations: University of Dortmund (D), IMEC (B)

Other sources of funding: CEE - ESPRIT Project CHIPS: Code-generation for Heterogeneous Information Processing Systems, MURST

Area: Integrated Circuits and Systems

8) **CODE GENERATION FOR HETEROGENEOUS COMPUTING ARCHITECTURES**

F. Curatelli, L. Mangeruca, M. Chirico

Collaborations: University of Dortmund (D), IMEC (B)

Other sources of funding: CEE - ESPRIT Project CHIPS: Code-generation for Heterogeneous Information Processing Systems, MURST

Area: Integrated Circuits and Systems

9) **FORMAL VERIFICATION OF CFSMS**

F. Curatelli, L. Mangeruca

Collaborations: Politecnico Torino, Cadence Berkeley labs, CA

Area: Integrated Circuits and Systems

10) **SPEECH RECOGNITION FOR MULTIMEDIA APPLICATIONS**

F. Curatelli, O. Mayora-Ibarra

Collaborations: IRST, TN

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